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Fig. 10B

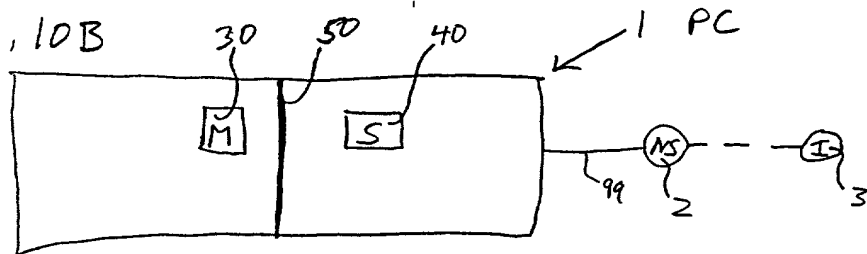
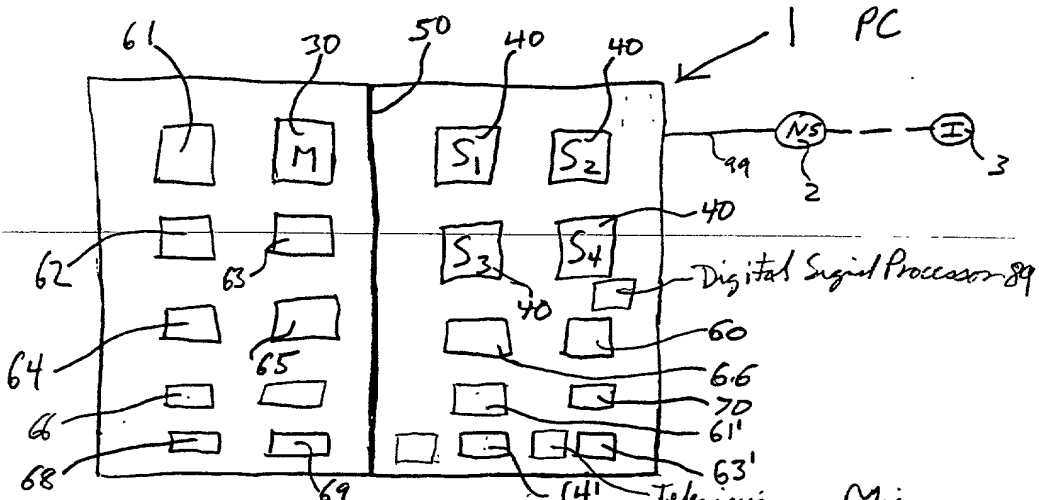
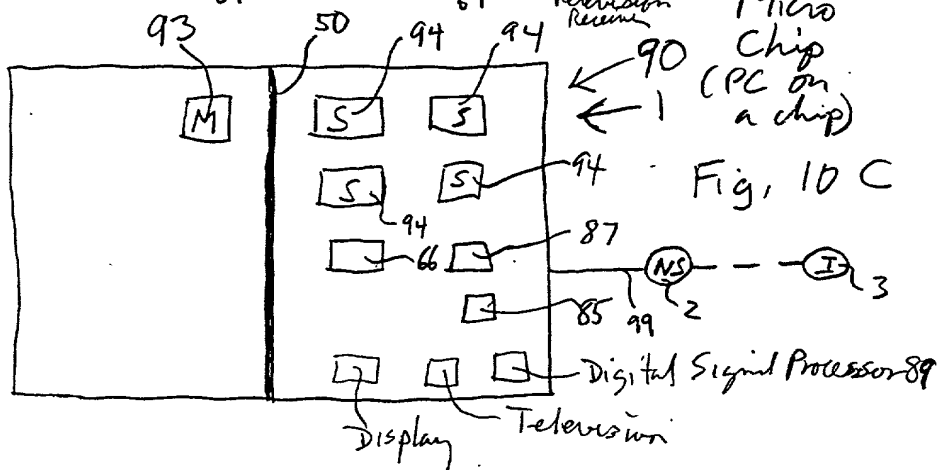


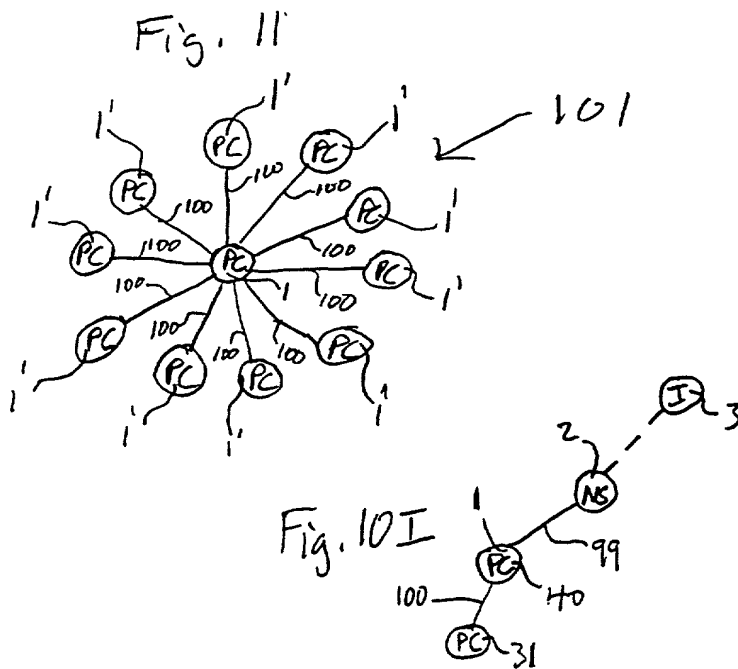
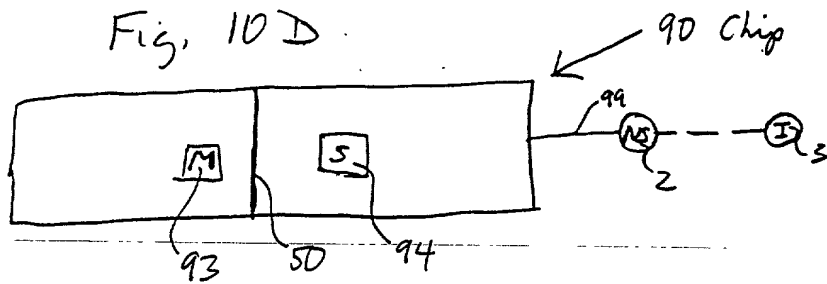
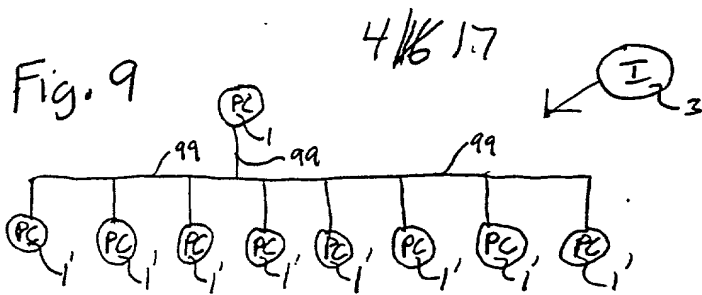
Fig. 10A



Micro Chip (PC on a chip)

Fig. 10C





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Fig. 10E

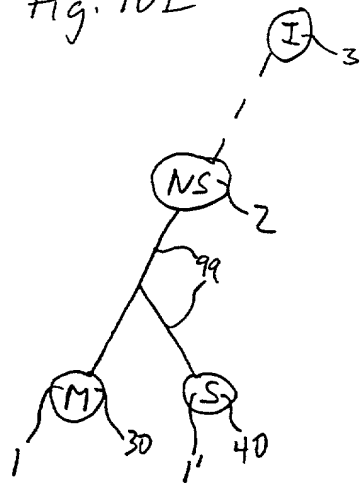


Fig. 10F



Fig. 10G

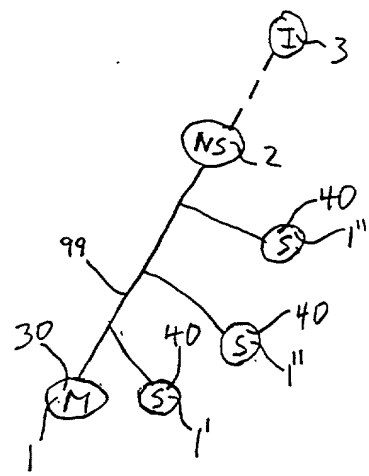
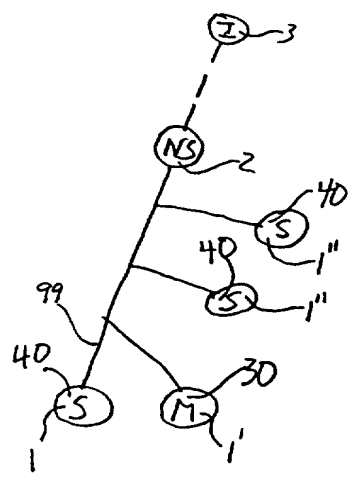
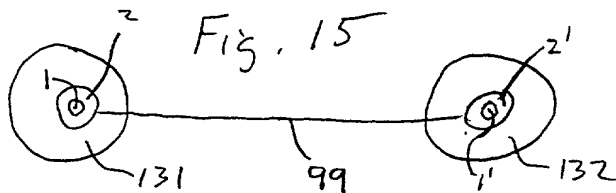
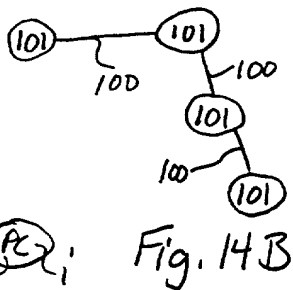
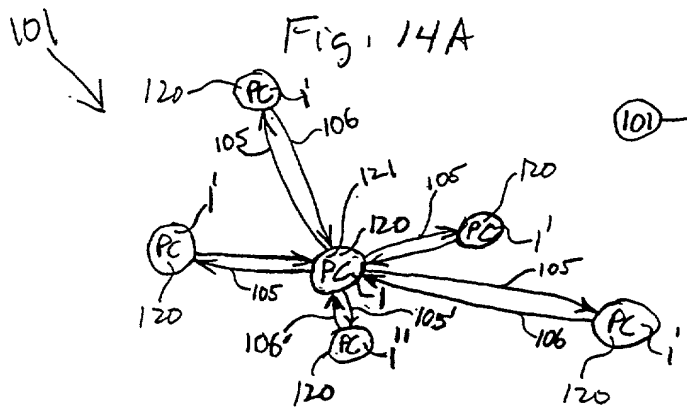
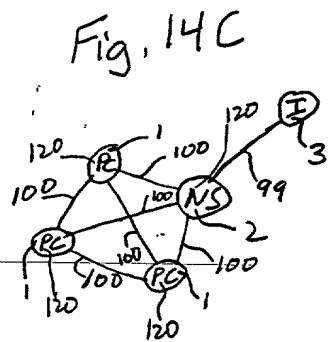
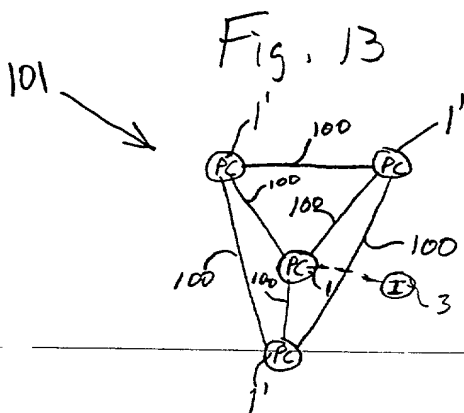
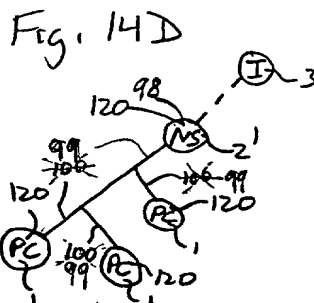
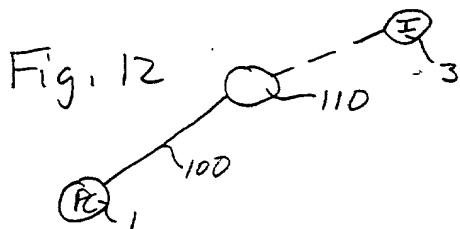


Fig. 10H



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Fig. 10K

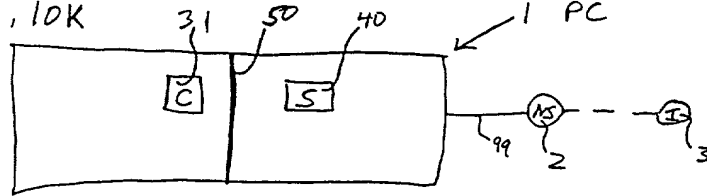


Fig. 10J

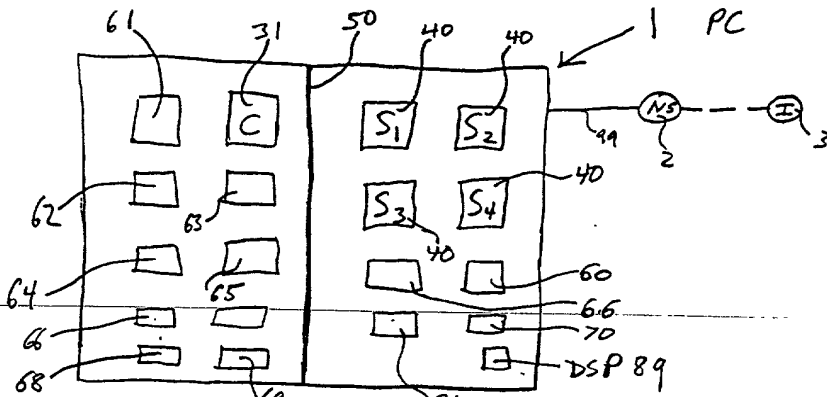


Fig. 10L

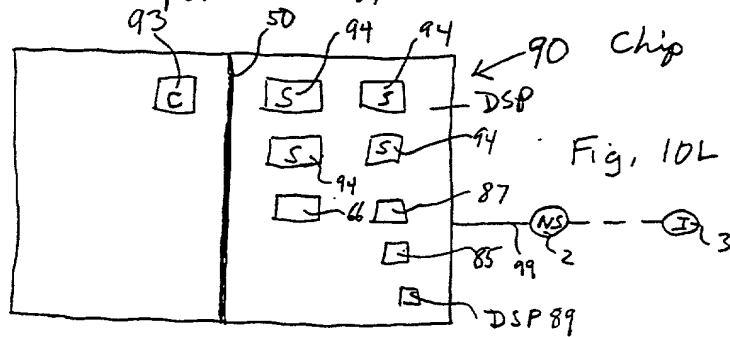
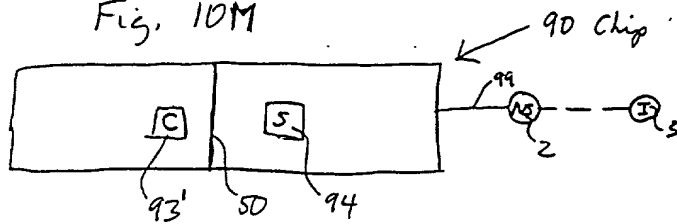


Fig. 10M



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Fig. 10 O

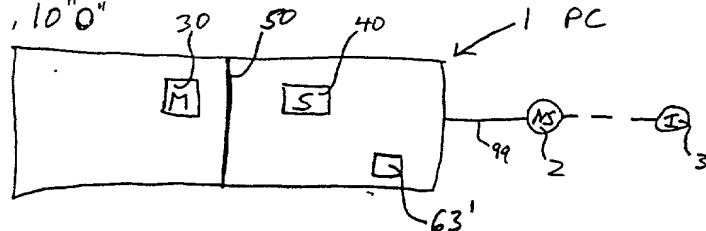


Fig. 10 N

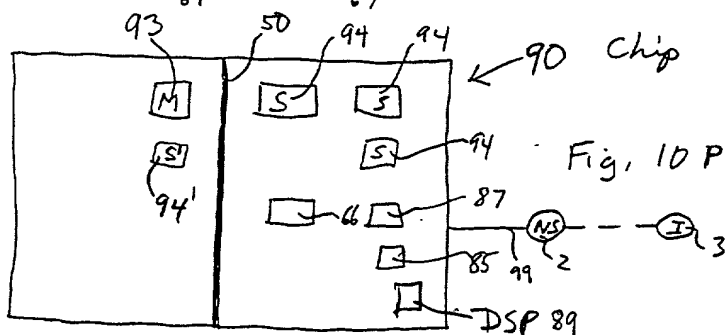
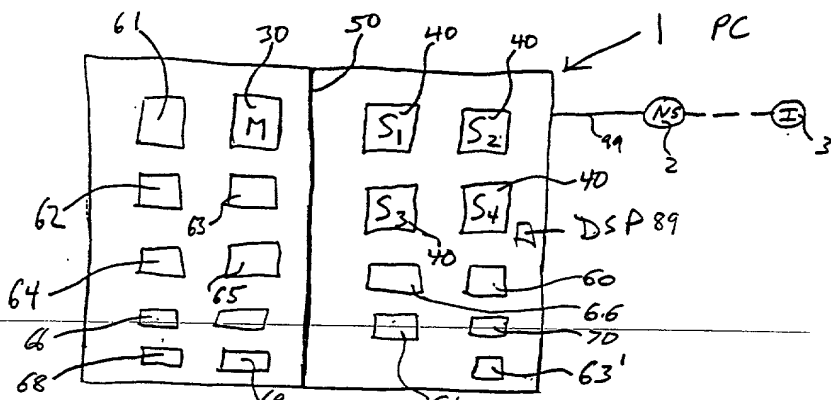
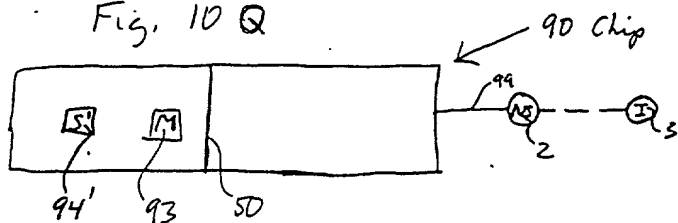


Fig. 10 Q





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Fig. 16A

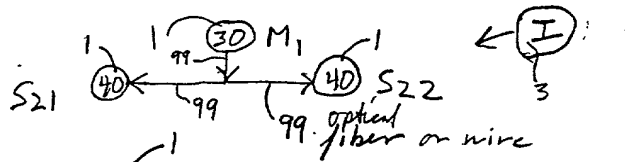


Fig. 16B

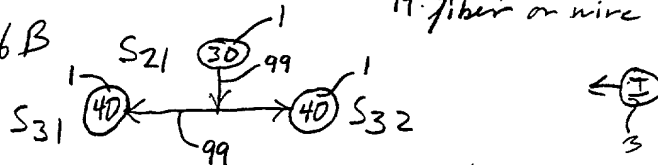


Fig. 16C

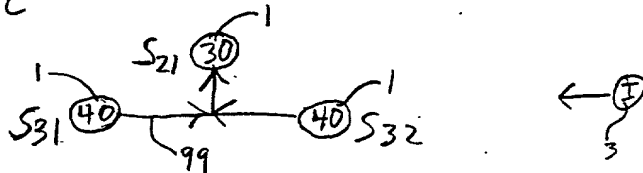


Fig. 16D

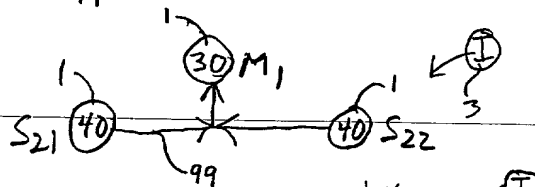


Fig. 16G



Fig. 16H

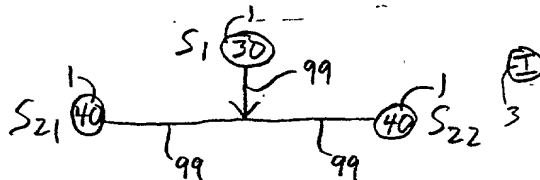


Fig. 16I

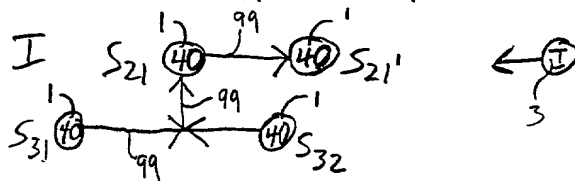


Fig. 16J





Fig. 16L

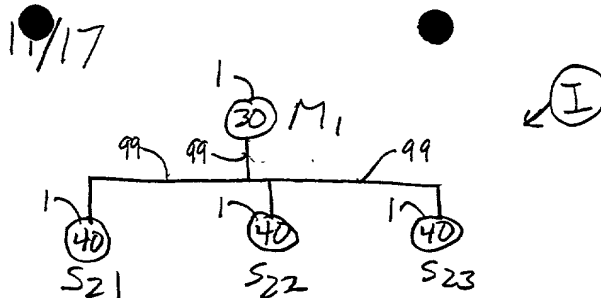


Fig. 16M

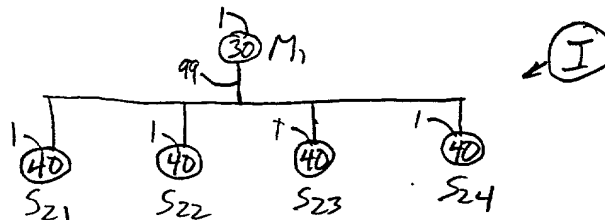


Fig. 16N

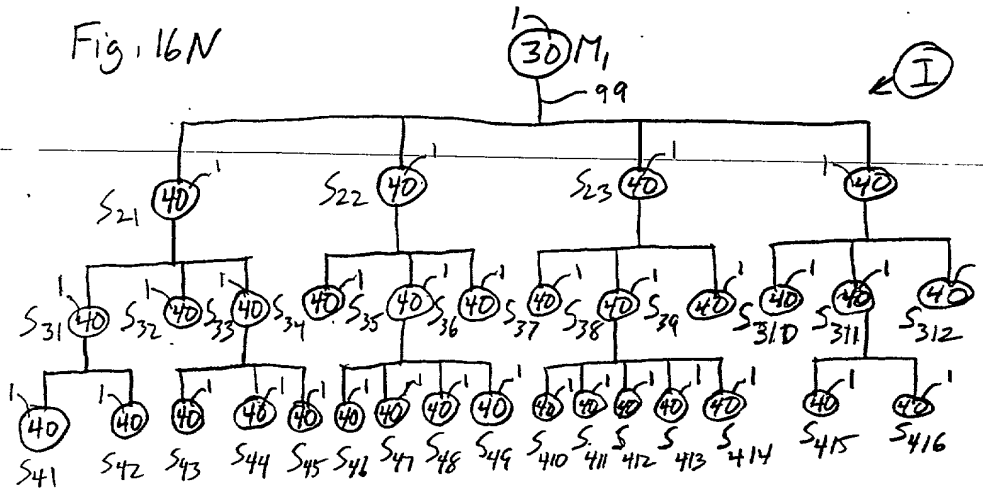


Fig. 16O

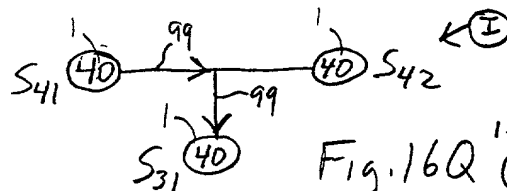


Fig. 16Q

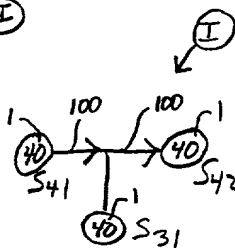
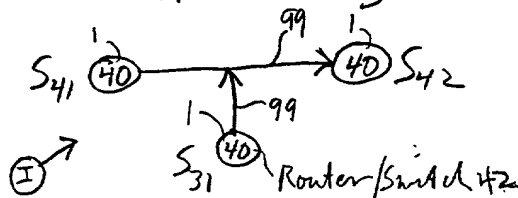


Fig. 16P



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Fig. 16X

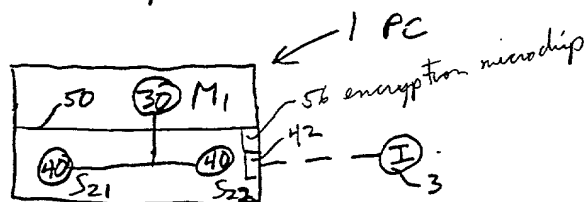


Fig. 16Y

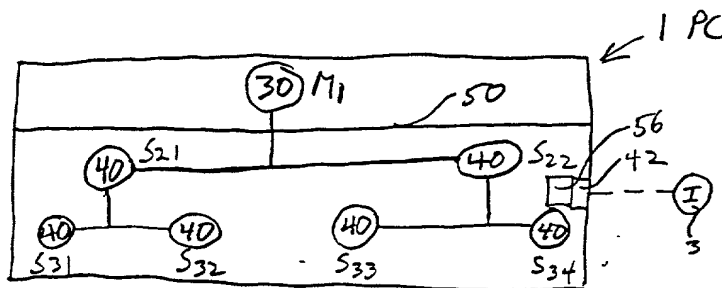


Fig. 16Z

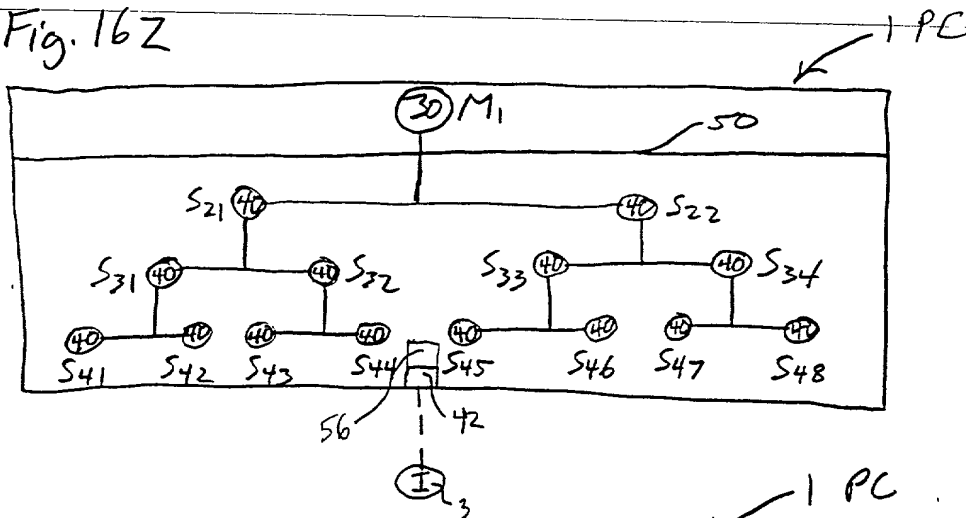
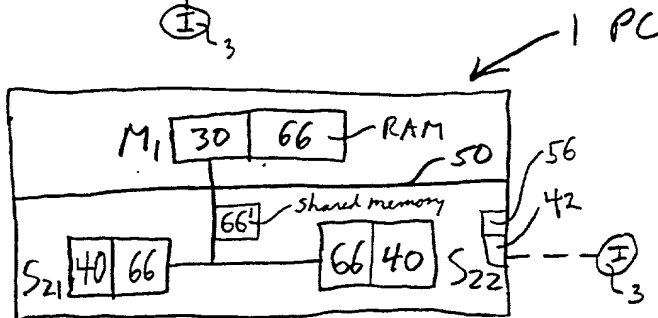


Fig. 16AA



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Fig. 16R

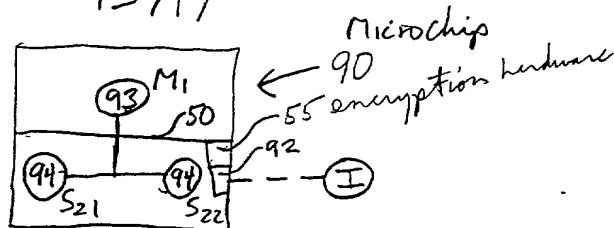


Fig. 16S

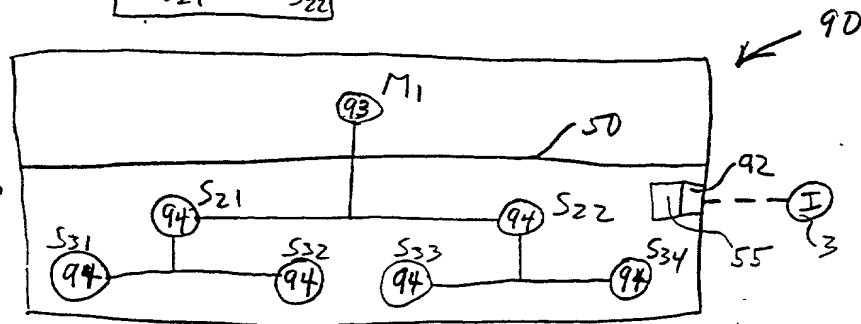


Fig. 16T

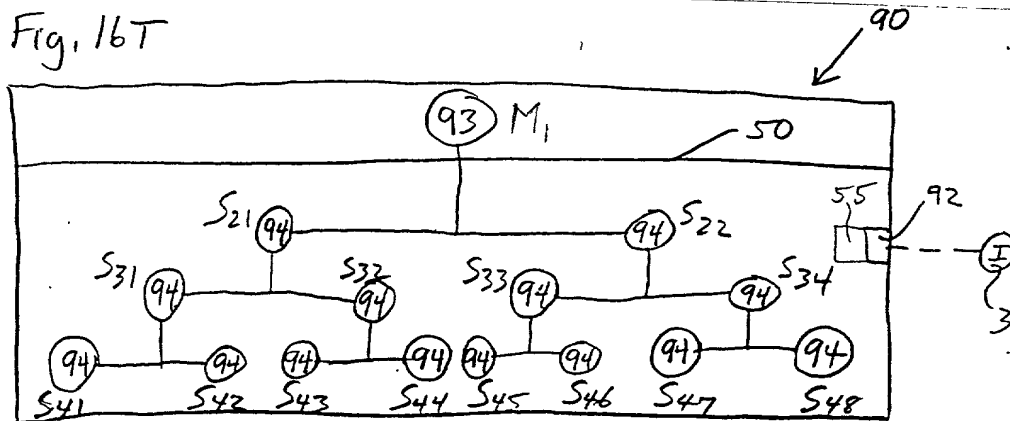
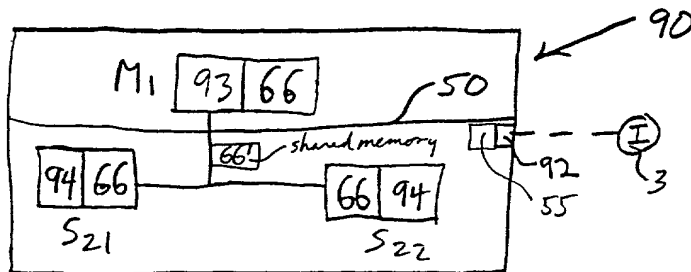
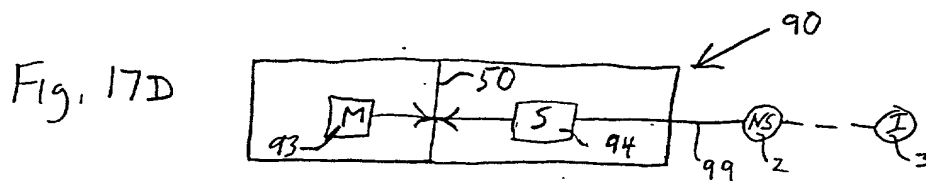
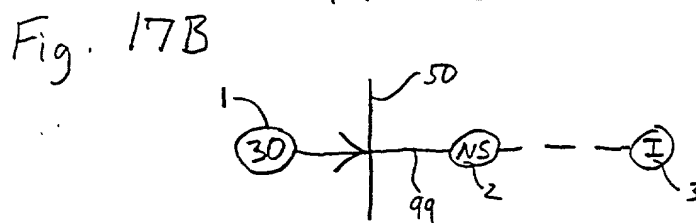
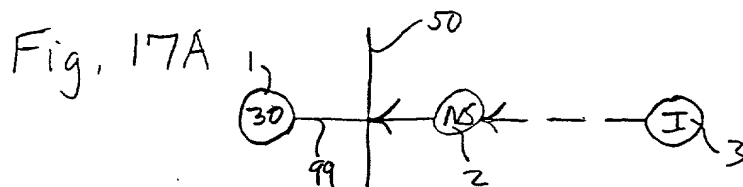
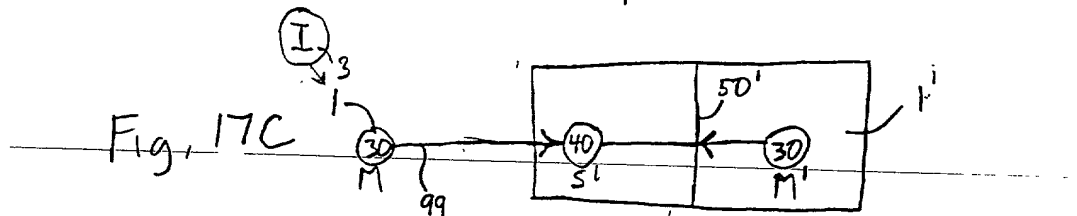
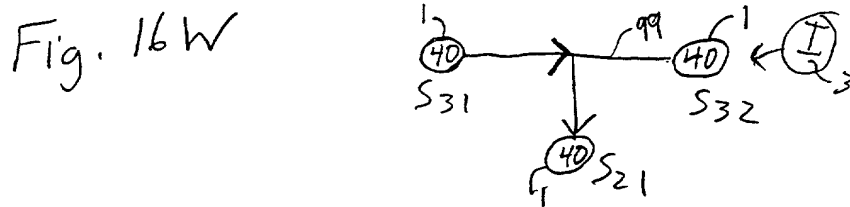
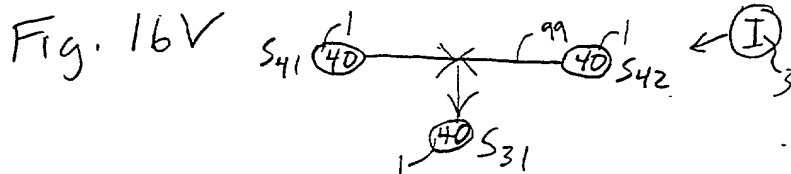


Fig. 16U



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Fig. 18A

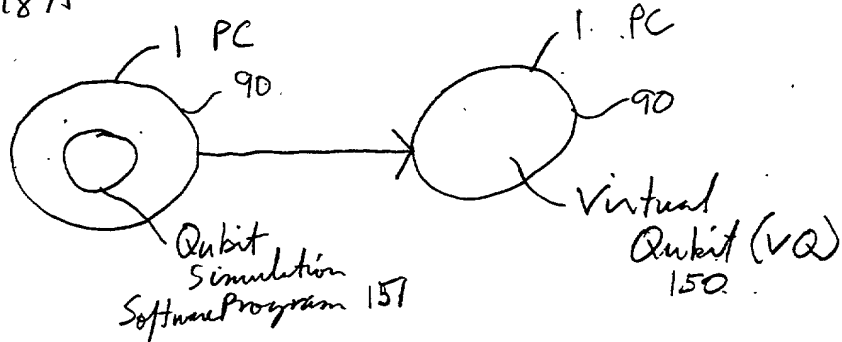


Fig. 18B

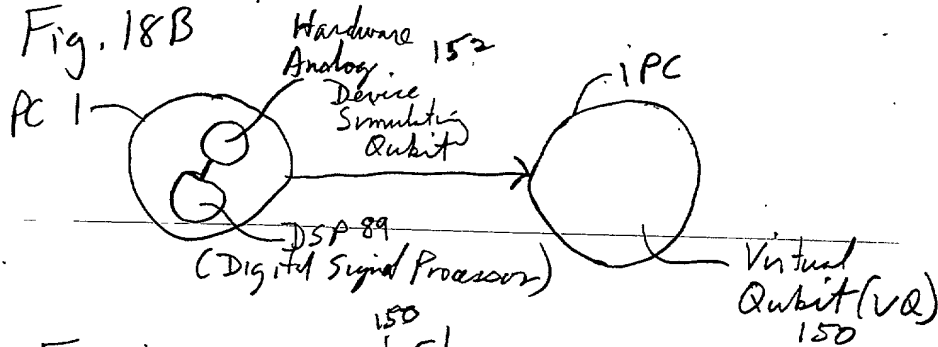


Fig. 18C

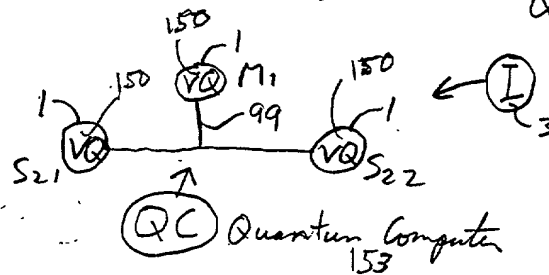
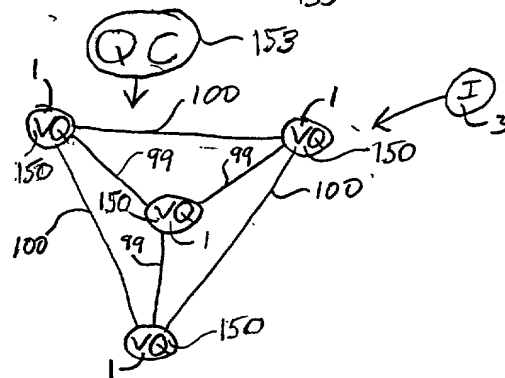


Fig. 18D



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Fig. 19

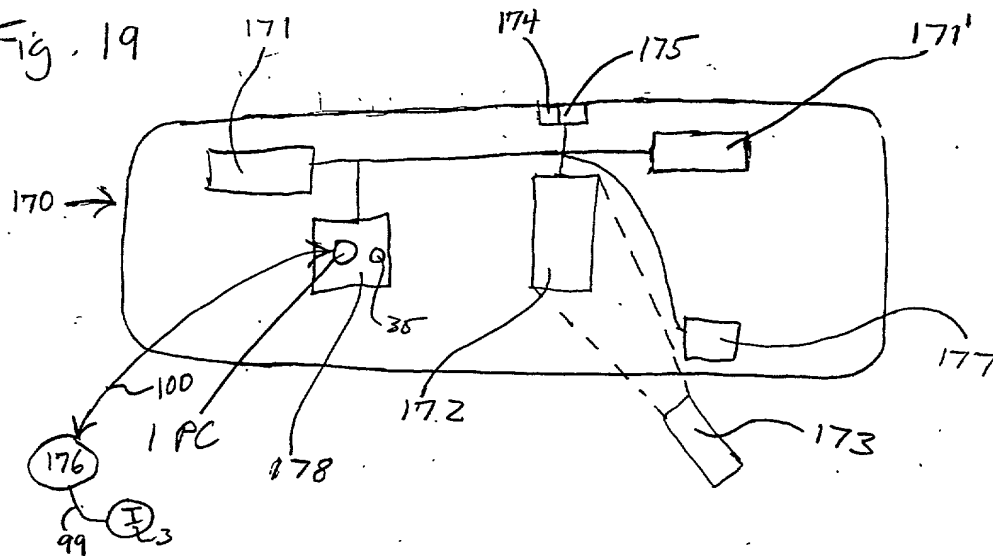


Fig. 16 AB

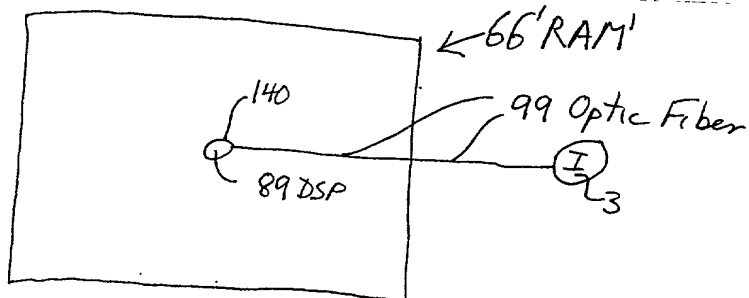




Fig. 20A

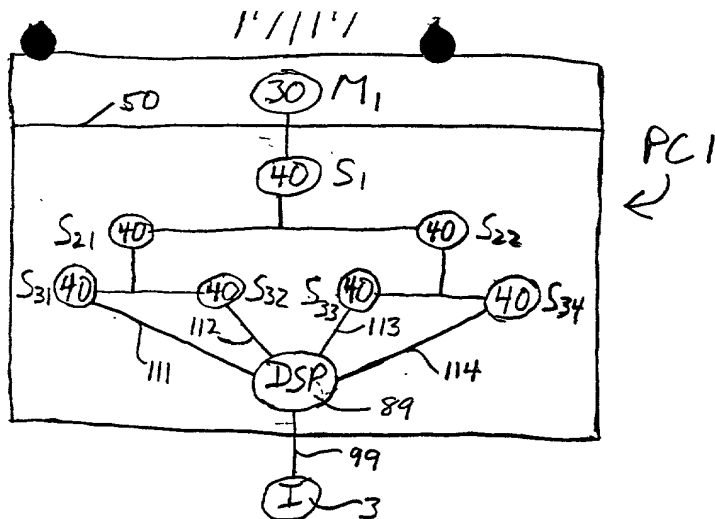


Fig. 20B

